

Attorney's Docket No. 5646-63

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Curt A. Karnstedt; Bruce L. Chin; Prashant Shamarao; Mario Montana

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: INTEGRATED CIRCUIT FIFO MEMORY DEVICES THAT ARE DIVISIBLE
INTO INDEPENDENT FIFO QUEUES, AND SYSTEMS AND METHODS FOR
CONTROLLING SAME

jc979 U.S. PTO
09/944799

08/30/01

August 30, 2001

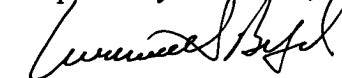
Box Patent Application
Commissioner for Patents
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,



Mitchell S. Bigel

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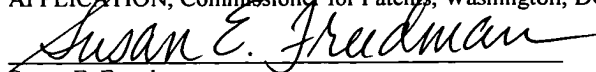
PATENT TRADEMARK OFFICE

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: BOX PATENT APPLICATION, Commissioner for Patents, Washington, DC 20231.



Susan E. Freedman

Date of Signature: August 30, 2001

Form PTO-1449		U.S. Department of Commerce Patent and Trademark Office		Attorney Dock No. 5646-63		Serial No. To Be Assigned	
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				Applicants: Curt A. Karnstedt; Bruce L. Chin; Prashant Shamaraao; Mario Montana			
				Filing Date: To Be Assigned			
				GAU:		U.S. PTO 09/944799 08/30/01	
U.S. PATENT DOCUMENTS							
	1	6,243,799	06/05/01	Chan et al.	711	212	
	2	6,233,651	05/15/01	O'Neill et al.	711	101	
	3	6,230,249	05/08/01	Chan et al.	711	212	
	4	6,122,717	09/19/00	Chan et al.	711	170	
	5	6,070,203	05/30/00	Hawkins et al.	710	57	
	6	5,999,478	12/07/99	Proebsting	365	230.05	
	7	4,847,812	07/11/89	Lodhi	365	221	
	8	4,750,149	06/07/88	Miller	364	900	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	9	Texas Instruments Incorporated, <i>4096 x 18 Clocked Multiple-queue (Multi-QTM) First-In, First-Out Memory With Three Programmable-Depth Buffers and Cell-Based Flags</i> , SN74ACT53861, 1995, 1 page					
	10	Au et al., U.S. Application Serial No. 09/721,478, Filed November 22, 2000					

Examiner: _____ Date Considered: _____

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.